

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	09915437			
<b>Filing Date:</b>	26-Jul-2001			
<b>Title of Invention:</b>	Method of logic circuit synthesis and design using a dynamic circuit library			
<b>First Named Inventor:</b>	Sang Hoo Dhong			
<b>Filer:</b>	Russell Duane Culbertson/Gracie Solis			
<b>Attorney Docket Number:</b>	AUS9-2001-0301US1			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Notice of appeal	1401	1	500	500
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				500